

## ABSTRACT

In an isolation trap circuit of the present invention, a PIN diode PD and an inductor L are connected in parallel to a first terminal 61, and a capacitor C1 is connected in series to the inductor L, while the PIN diode PD and the capacitor C1 are connected in parallel to a second terminal 62. The isolation trap circuit is characterized in that a capacitor C2 is further connected and grounded between the first terminal 61 and the inductor L, or between the inductor L and the capacitor C1, or between the capacitor C1 and the second terminal 62.